

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: )  
DAISUKE MIURA, et al. )  
Application No.: 10/555,614 )  
371(c) Date: November 4, 2005 )  
For: FIELD EFFECT TRANSISTOR, )  
METHOD OF PRODUCING )  
THE SAME, AND METHOD )  
OF PRODUCING LAMINATED )  
MEMBER ) December 12, 2007

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

RESPONSE TO RESTRICTION REQUIREMENT

Sir:

In response to the restriction requirement set forth in the Office Action dated November 26, 2007, Applicants provisionally elect to prosecute the Group I claims, namely Claims 1 to 10 and 37. The election is made without traverse.

Applicants' undersigned attorney may be reached in our Costa Mesa, California office at (714) 540-8700. All correspondence should continue to be directed to our below-listed address.

Respectfully submitted,

/Damond E. Vadnais/  
Damond E. Vadnais  
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